2402 Michelson Drive Suite 210 Irvins, CA 92612 T; 949-752-7040 F: 949-752-7049

MacPherson Kwok Chen & Heid LLP

RECEIVED **CENTRAL FAX CENTER** Date: SEP 0 5 2006 September 5, 2006 571-273-8300 Fax Telephone #: U.S. Patent and Trademark Office To: Office Telephone #: Date Sent: Jonathan W. Hallman From: Applicant: Moyer et al. Time Sent: Subject: Serial No. 10/676,494 Fax Operator: M-15194 US Client/File:

This transmittal consists of 14	total page(s), including this cover sheet.
-----------------------------------	--

Message:

If you do not receive all pages, please call (949) 752-7040

THE INFORMATION CONTAINED IN THIS FACSIMILE MESSAGE IS INTENDED ONLY FOR THE PERSONAL AND CONFIDENTIAL USE OF THE DESIGNATED RECIPIENT(S) NAMED ABOVE. THIS MESSAGE MAY BE AN ATTORNEY-CLIENT COMMUNICATION, AND AS SUCH IS PRIVILEGED AND CONFIDENTIAL. IF THE READER OF THIS MESSAGE IS NOT THE INTENDED RECIPIENT OR AN AGENT RESPONSIBLE FOR DELIVERING IT TO THE INTENDED RECIPIENT, YOU ARE HEREBY NOTIFIED THAT YOU HAVE RECEIVED THIS DOCUMENT IN ERROR AND THAT ANY REVIEW, DISSEMINATION, DISTRIBUTION OR COPYING OF THIS MESSAGE IS STRICTLY PROHIBITED. IF YOU MAVE RECEIVED THIS COMMUNICATION IN ERROR, PLEASE NOTIFY US IMMEDIATELY BY TELEPHONE AND RETURN THE ORIGINAL MESSAGE TO US BY MAIL. THANK YOU.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Mark Moyer & Jeffrey Byrne

RECEIVED CENTRAL FAX CENTER

Assignee:

Lattice Semiconductor Corporation

SEP 0 5 2006

Title:

Continuous Self-Verify Of Configuration Memory In Programmable

Logic Devices

Serial No.:

10/676,494

Filing Date:

09/30/2003

Examiner:

Group Art Unit:

Mujtaba Chaudry

2124

Docket No.:

M-15194 US

Confirmation

8571

No.:

Irvine, California September 5, 2006

FAX NO. (571) 273-8300

Mail Stop: Amendment Commissioner for Patents

P.O. Box 1405

Alexandria, VA 22313-1450

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following Transmittal Letter (1 page) and Response to Office Action (11 pages) are being facsimile transmitted to the Patent and Trademark Office on the date shown below.

Dated: September 5, 2006

Liva Kuy

MacPherson Kwok Chen & Heid LLP 1762 Technology Drive Suite 226

San Jose, California 95110 Telephone: (949) 752-7040 Fax: (408) 392-9262

Lavy offices of Eactherson Kwok Chen B neid Lip

52 TECHNOLOGY DRIVE SUITE 226 SAN JOSE, CA 95110 (949) 752-7040 FAX (408) 392-9252

Serial No. 10/676,494

MacPherson Kwok Chen & Heid LLP

1762 Technology Drive, Suite 226

San Jose, CA 95110 Tel. (408) 392-9250 Fax (408) 392-9262 2402 Michelson Drive, Suite 210 Irvine, CA 92612 Tel. (949) 752-7040

Fax (949) 752-7049

Email: jhallman@ macpherson-kwok.com www.macpherson-kwok.com

RECEIVED **CENTRAL FAX CENTER** SEP 0 5 2006

September 5, 2006

Commissioner For Patents P.O. Box 1450 Alexandria, VA 22313-1450\

Re:

Applicant(s):

Mark Moyer and Jeffrey Byme

Assignee: Title:

Lattice Semiconductor Corporation

Scrial No.:

Continuous Self-Verify of Configuration Memory in Programmable Logic Devices

10/676,494 Mujtaba Chaudry

September 30, 2003 Filed:

Examiner: Docket No .: M-15194 US

Group Art Unit: 2133 Confirmation No. 8571

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

- Facsimile Cover Sheet; (1)
- This Transmittal Letter (1 page)
- Response to Office Action (11 pages)

No additional fee is required. The fee has been calculated as shown below:

CLAIMS AS AMENDED

		Claims Remaining Af	<u>Per</u>	Highest No. Previously <u>Paid</u> <u>For</u>		Present Extra		Raje		Additional <u>Fee</u>		
Total Claims		20	Minus	24	=	0	×		\$ \$		0	
Independent Claims			Minus									
	Fee of _ per appl	for the first filing o	f one or more m	ultiple dependent c	laims				\$			
		Total additional fee	for this Amend	ment:					\$		Q	
	Fee for Request for Continued Examination										Ū	
☒	Conditional Potition for Extension of Time: If an extension of time is required for timely filing of the enclosed document(s) after all papers filed with this transmittal have been considered, an extension of time is hereby requested.											
	Please charge our Deposit Account No. 50-2257 in the amount of								\$		<u>0</u>	
Ø	Also, charge any additional fees required and credit any overpayment to our Deposit Account No. 50-2257											
							т	otal:	\$		<u>o</u>	
		Certificate of Trans	nission		_		- 0.2					

I heroby certify that this correspondence is facsimile transmitted to the Commissioner for Patents, Alexandria, VA 22313-1450, at) 273-8300 on the date shown below.

Tina Kavanaugh

September 5, 2006

Respectfully submitted,

Jonathan W. Hallman Attorney for Applicants

Reg. No. 42,622

M-15194 US 10/676,494

RECEIVED CENTRAL FAX CENTER

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

SEP U 5 2006

Applicant(s):

Mark Moyer, et al.

Title:

Continuous Self-Verify of Configuration Memory in Programmable

Logic Devices

Serial No.:

10/676,494

Filing Date:

September 30, 2003

Examiner:

Mujtaba Chaudry

Group Art Unit:

2133

Docket No.:

M-15194 US

Confirmation No.:

8571

Irvine, California September 5, 2006

Via Facsimile to (571) 273-8300

Mail Stop AMENDMENT COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION

Dear Sir:

In response to the Office Action mailed on June 2, 2006, please amend the aboveidentified application as follows: